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Sheet

1

of

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Application Number	09/589,621
Filing Date	06/07/2000
First Named Inventor:	Sara Ruhina Biyabani
Art Unit	2628
Examiner Name	Caschera, Antonio A.
Attorney Docket No.	04860.P2438

[illegible]

Exmnr Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ^o
		Country Code ³	Number ⁴				
/AC/		EP	0525986	A2	02/03/1993	Priem, Curtis et al.	

Examiner
Signature

/Antonio Caschera/

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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	09/589,621
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Sheet	2	of	2	Attorney Docket No.	04860.P2438
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
/AC/		JUPITERMEDIA CORPORATION. "Integrated Video: Breaking the last PC Taboo," 802.11 Planet Conference & Expo World Tour, downloaded from the Internet at: www.cpuplanet.com/features/article.php/1566521 , © 2003 Jupitermedia Corporation (2 pages).			
/AC/		KOC SIS, DAVID. "Frame-buffer wars: new directions in PC graphics," <u>EDN Access for Design, by Design</u> , May 9, 1996 (10 pages).			
/AC/		SILICON GRAPHICS, INC. "O ² Unified Memory Architecture," <u>Silicon Graphics, Inc.</u> , 1997 (pp. 1-6).			
/AC/		WILLIAMS, IAN. "An Explanation of the Advantages Afforded by QED RM5200 and MIPS R12000 CPUs in O2," SGI White Paper, 1999 (pp. 1-41).			

Examiner Signature	/Antonio Caschera/	Date Considered	04/26/2007
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